

## **ABSTRACT OF THE DISCLOSURE**

A power-saving circuit for an active matrix liquid crystal display ("LCD") panel that comprises a plurality of first capacitors, each first capacitor corresponding to a data line of the LCD panel for collecting electrical charge provided on an associated data line, at least one set of second capacitors, at least one set of transistors, each transistor of a set corresponding to one of the plurality of first capacitors, and at least two control signals, each control signal corresponding to a set of the at least one set of transistors and corresponding to a set of the at least one set of second capacitors, and each control signal functioning to switch between a first and a second state to control the operation state of an associated set of transistors, wherein the at least two control signals switch to a first state in a first sequence starting from a first control signal to a last control signal, and then in a second sequence starting from the last control signal to the first control signal, the first sequence alternating with the second sequence.

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